

FI9-98-027DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Gambino, et al.

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: FLASH MEMORY STRUCTURE USING SIDEWALL FLOATING GATE AND
METHOD FOR FORMING THE SAME

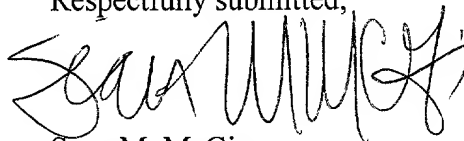
Assistant Commissioner of Patents
Washington, D.C. 20231

SUBMISSION OF PROPOSED DRAWING CORRECTIONS

Sir:

Submitted herewith is a corrected formal drawing for Figure 4 which have been approved in the parent application. Please substitute the attached drawing Figure 4 for the original drawing Figure 4 which was filed with the application.

Respectfully submitted,



Sean M. McGinn

Registration No. 34,386

Date: 11/08/01

McGinn & Gibb, PLLC

Intellectual Property Law

8321 Old Courthouse Road, Suite 200

Vienna, Virginia 22182-3817

(703) 761-4100

Customer No. 21254

FIG.1

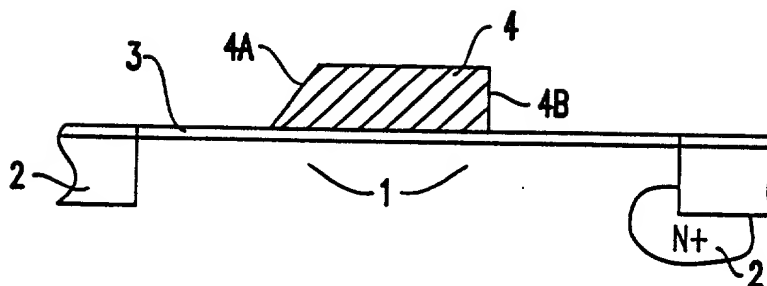


FIG.2

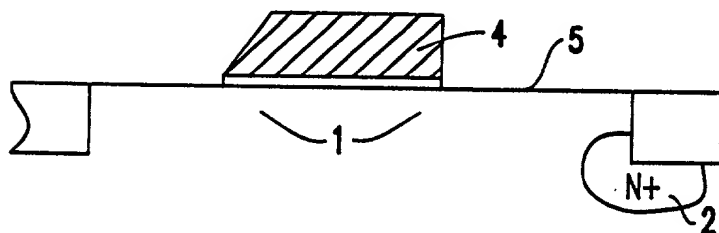


FIG.3

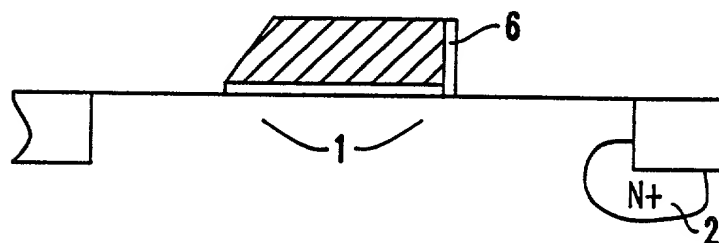


FIG.4B

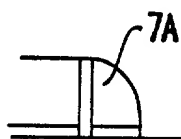
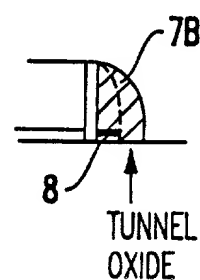


FIG.4C



DETAILS

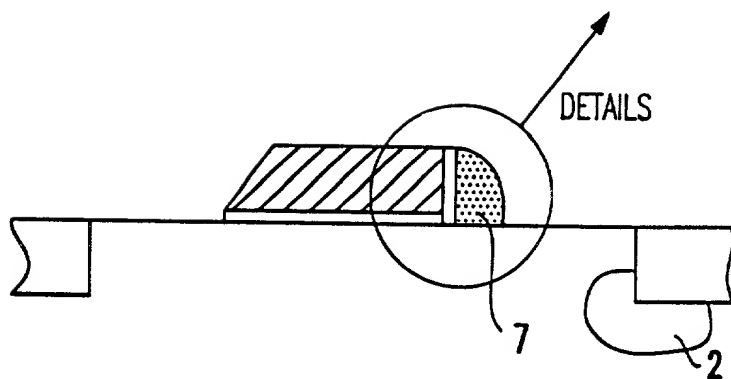


FIG.4A

FIG.1

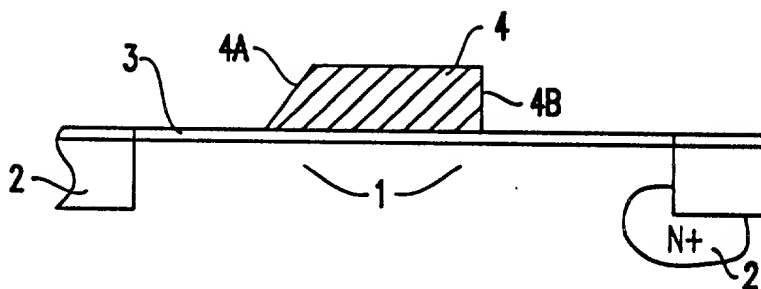


FIG.2

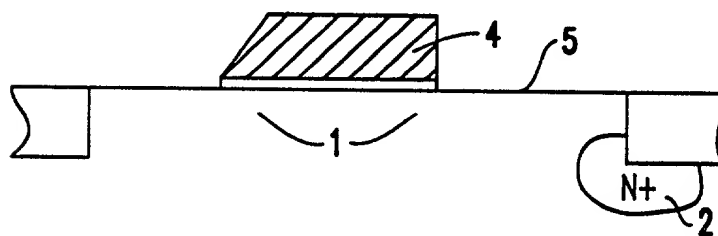


FIG.3

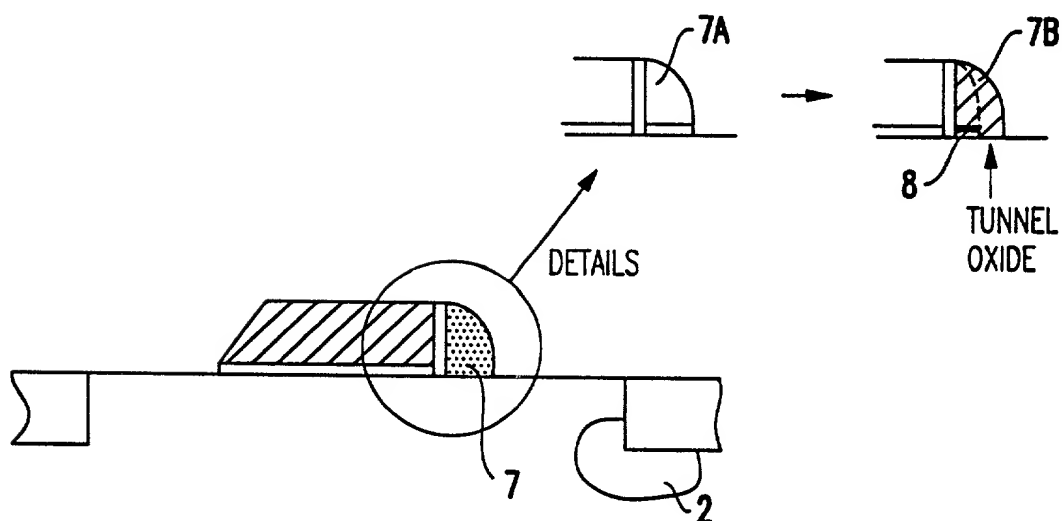
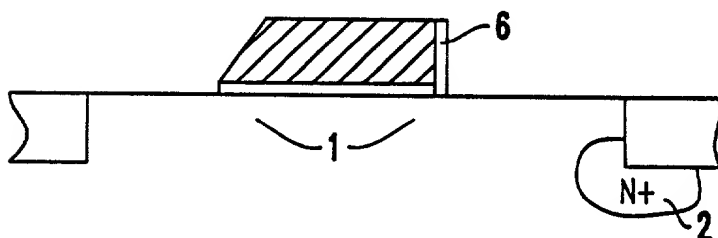
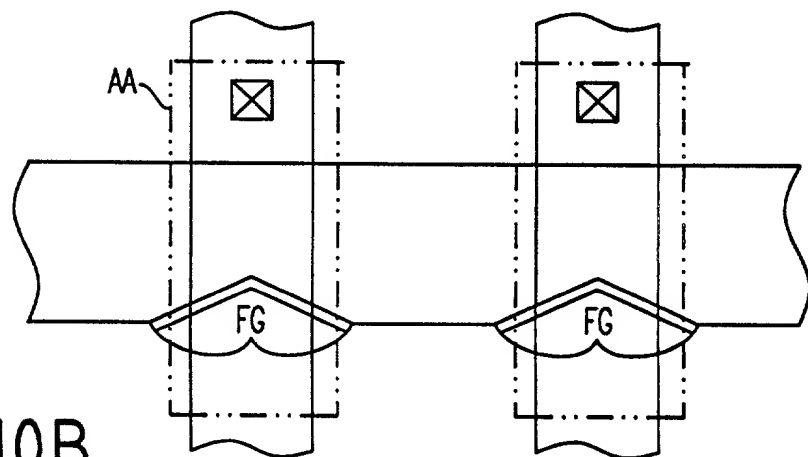
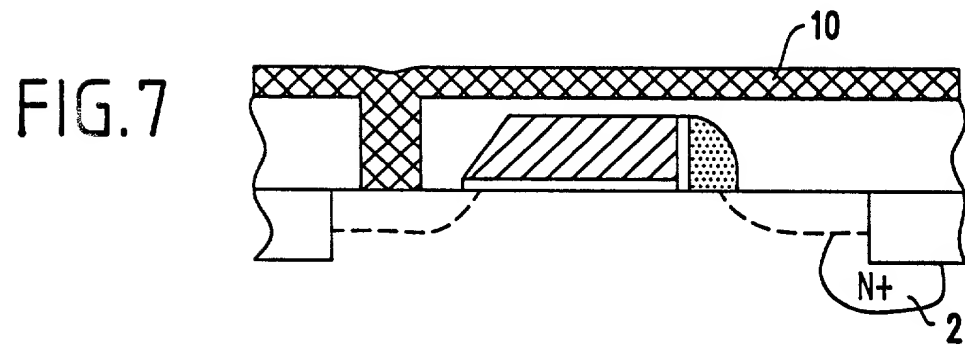
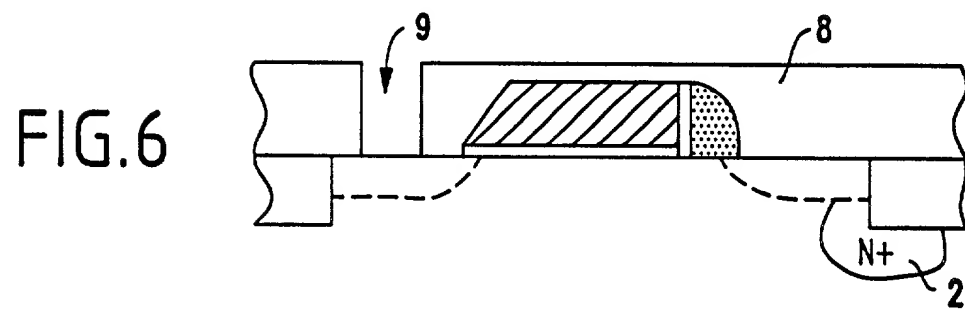
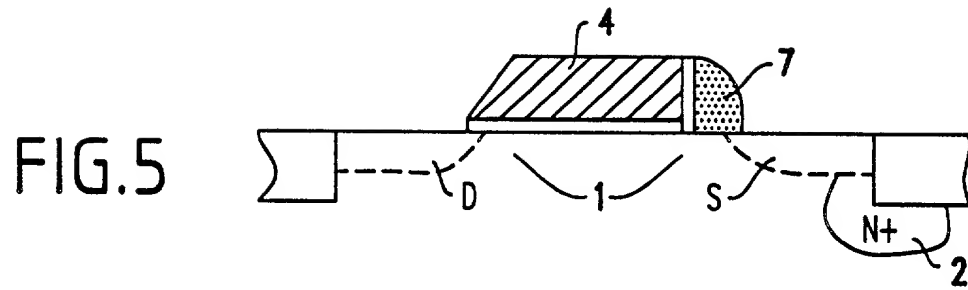
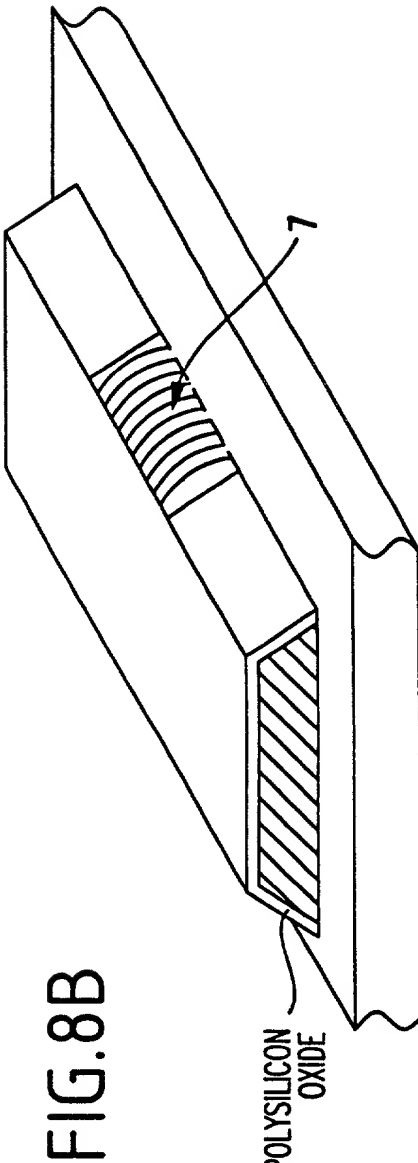
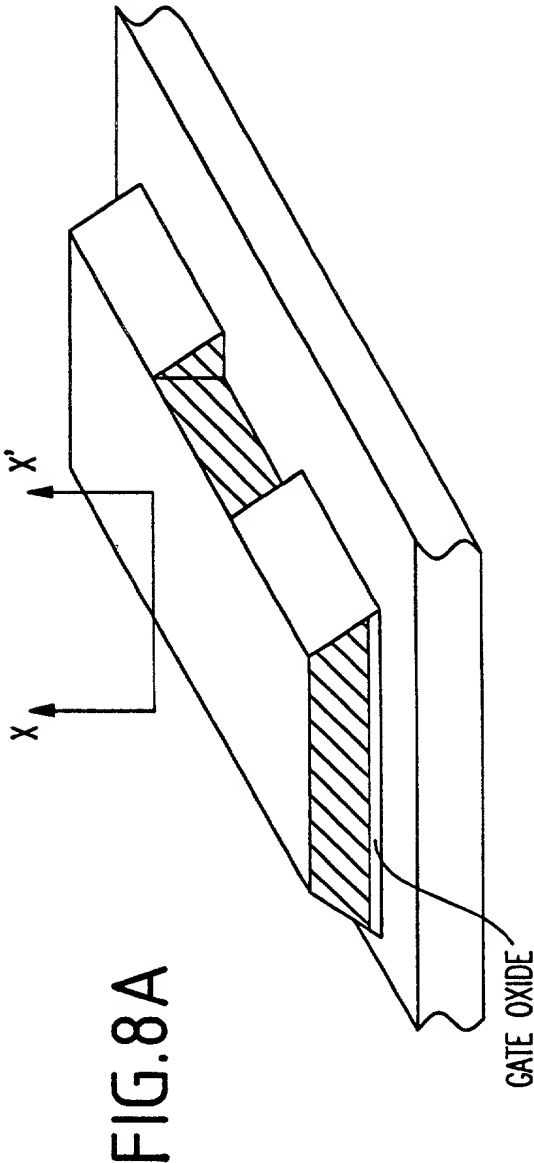


FIG.4





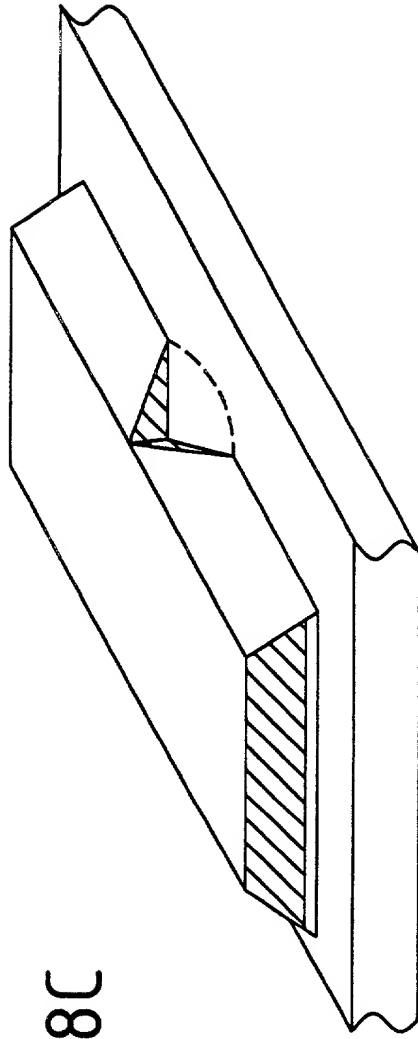


FIG. 8C

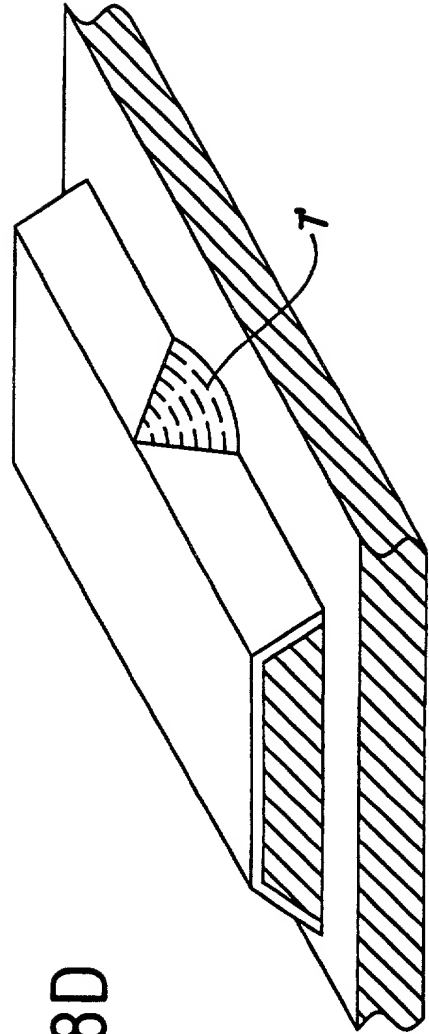


FIG. 8D

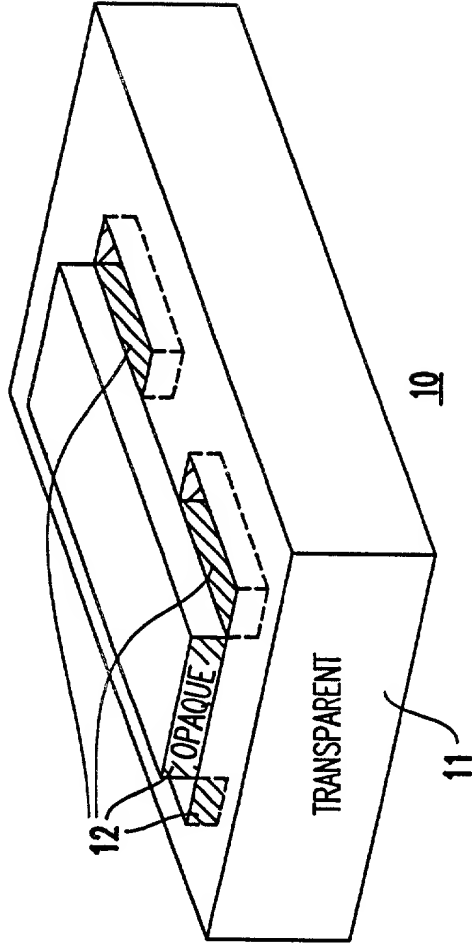


FIG. 9A

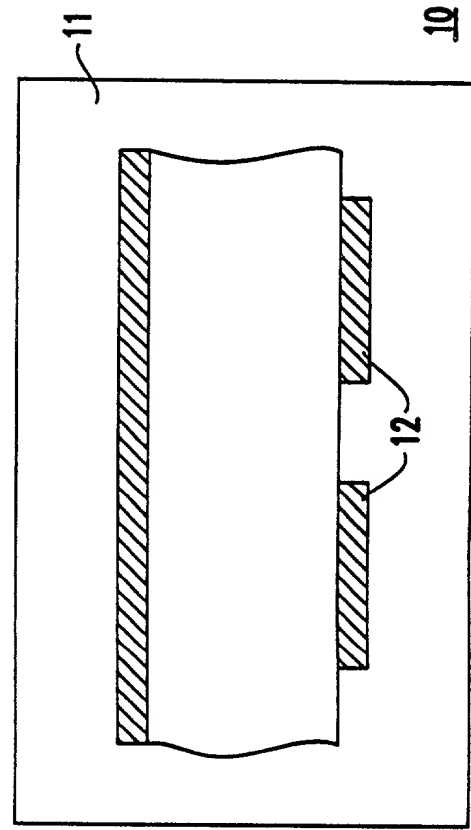


FIG. 9B

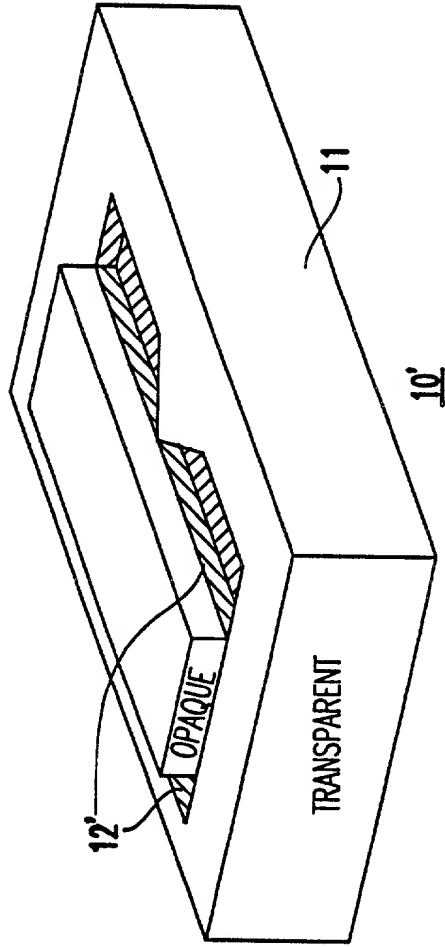


FIG. 9C

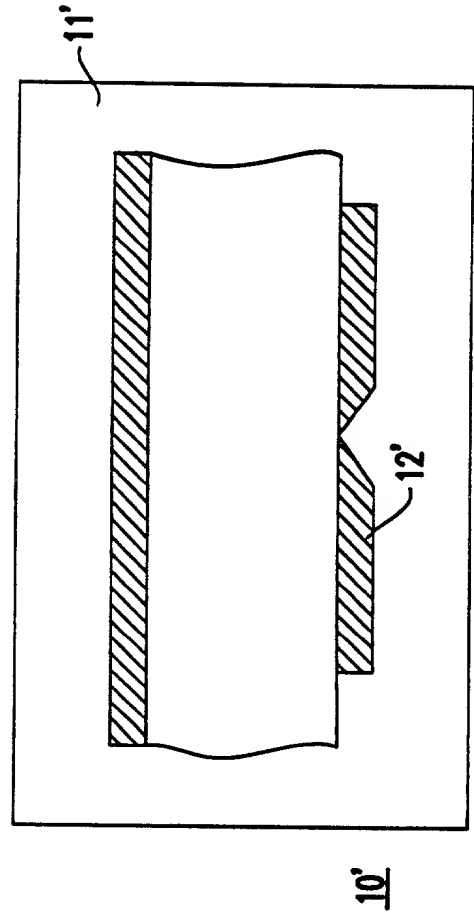


FIG. 9D

FIG. 10A

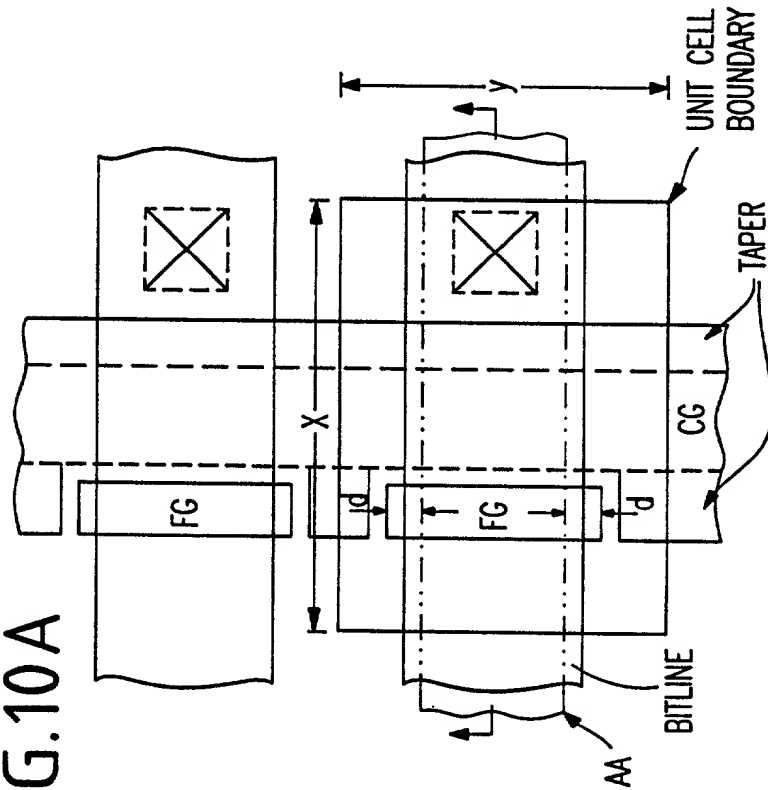


FIG. 12

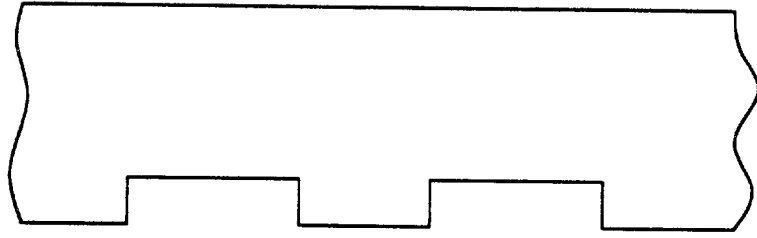
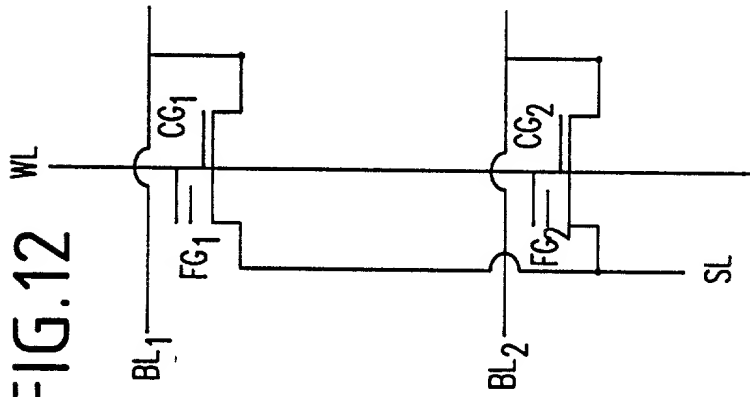


FIG. 21

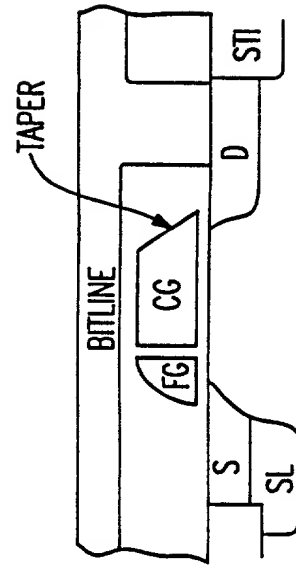


FIG. 11

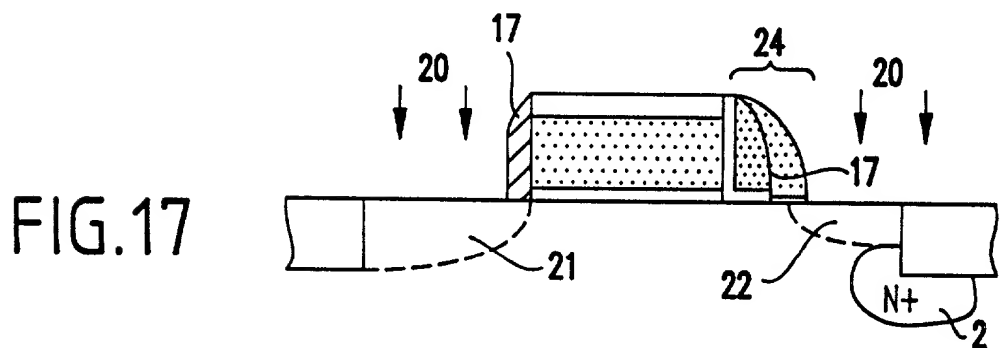
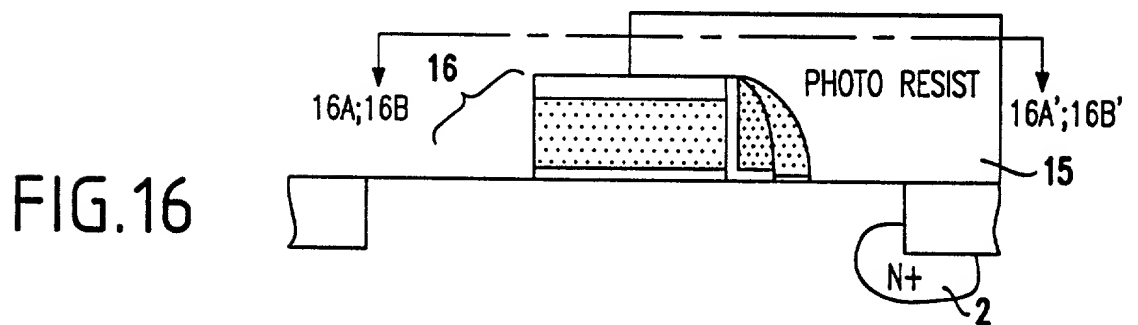
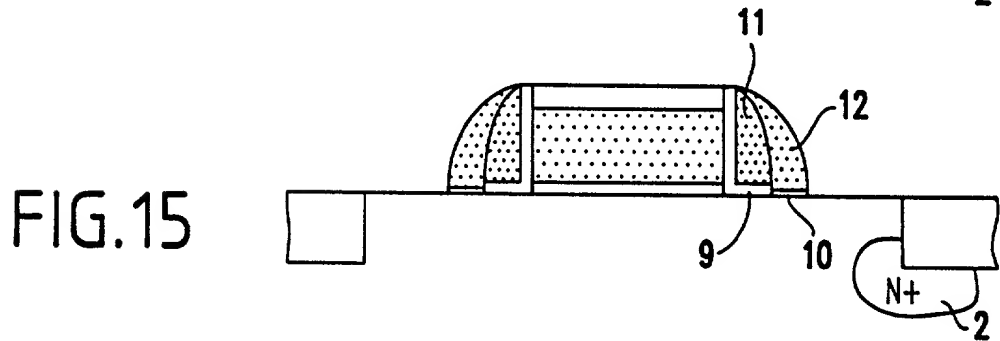
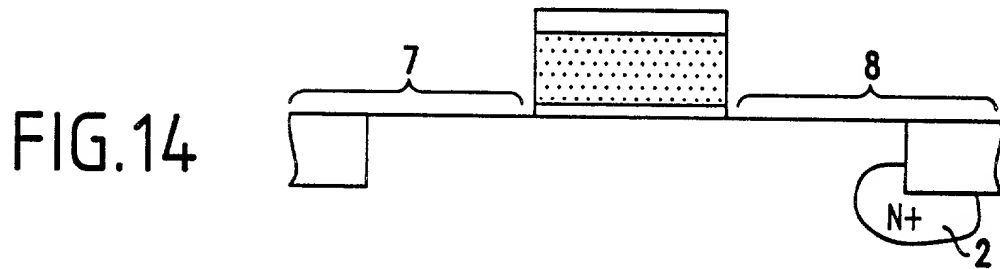
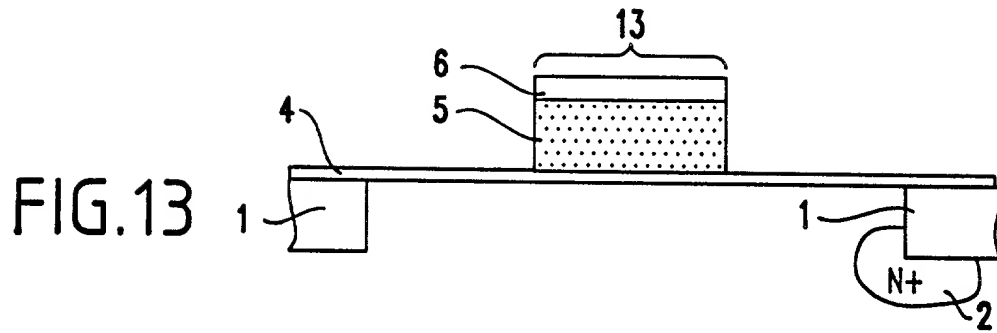


FIG.16A

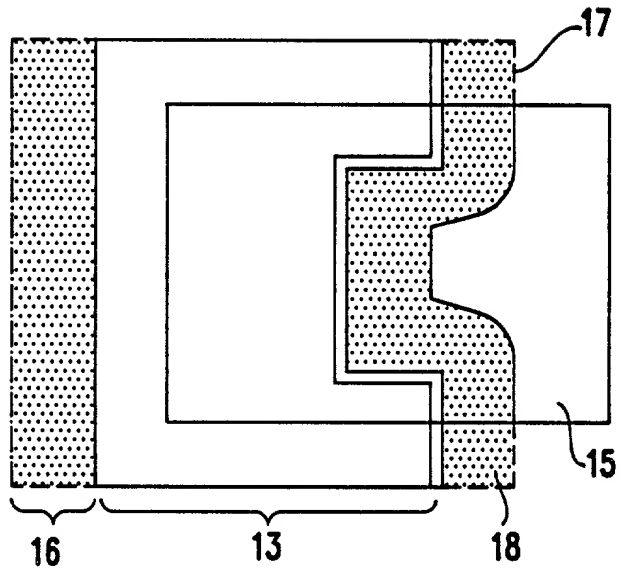


FIG.16B

